

ABSTRACT

A video display apparatus comprises a luminance signal processing circuit, a color-difference signal processing circuit, an RGB matrix circuit, a CRT drive circuit, a plurality of VM coils, a plurality of scanning-speed modulation circuit blocks, a horizontal deflection circuit, a vertical deflection circuit, a horizontal deflection coil, and a vertical deflection coil. An electron beam emitted 5 inside the CRT by the CRT drive circuit is scanned horizontally and vertically by the horizontal deflection coil and the vertical deflection coil. Velocity modulation currents are supplied to the plurality of VM coils by the plurality of scanning-speed modulation circuit blocks. This 10 results in the generation of velocity modulation magnetic fields from the plurality of VM coils, thereby partially modulating the velocity of the horizontally scanned electron beam.

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